

27. (Amended) A data processing system comprising:  
a first bus;  
a memory connected to the first bus;  
a general purpose processor (GPP) connected to the first bus, the GPP loading  
operands into the memory; and  
a digital signal processor (DSP) connected to the first bus, the DSP having a register  
and executing an instruction in response to the GPP loading information into the register, the  
information loaded into the register identifying the instruction, the DSP only executing a  
single instruction when said information is loaded into the register.

36. (Amended) A data processing system comprising:  
a first bus;  
a memory connected to the first bus;  
a general purpose processor (GPP) connected to the first bus, the GPP loading  
operands into the memory; and  
a digital signal processor (DSP) connected to the first bus, the DSP having a register,  
executing an instruction in response to the GPP loading information into the register, and  
retrieving operands required by the instruction from the memory by processing the  
information loaded into the register, the DSP only executing a single instruction when said  
information is loaded into the register.